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PATENT  
Docket No. SPLX.P0050

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Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on August 18, 2004.

  
John Stattler

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In the application of:

Li et al.

Serial No.: 09/854,146

Patent No.: 6,721,929 *B2*

Filing Date: 05/11/2001

For: HIGH ACCURACY TIMING MODEL  
FOR INTEGRATED CIRCUIT  
VERIFICATION

**Certificate**  
**AUG 27 2004**  
**of Correction**

**TRANSMITTAL LETTER FOR REQUEST FOR CERTIFICATE OF CORRECTION**

Attention Certificate of Corrections Branch  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:


Attached, please, find a Request for Certificate of Correction on PTO/SB/44 Form. There is no fee associated with this Request as the corrected error is not the fault of the Applicant. However, in the unlikely event that the Patent Office determines that additional fee is required,

**AUG 27 2004**

Applicant authorizes the Assistant Commissioner to charge the cost of such fees to **Deposit**  
**Account No. 50-1128** referencing SPLX.P0050.

Dated: August 18, 2004

Respectfully submitted,

By:   
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**UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION**

PATENT NO. : 6,721,929 B2

DATED : April 13, 2004

INVENTOR(S) : Li et al.

It is certified that error appears in the above-identified patent and that said Letters  
Patent is hereby corrected as shown below:

In the claims, column 12, line 27, "and bases on an effective driving current" should read -  
--and based on an effective driving current--.

MAILING ADDRESS OF SENDER: Stattler, Johansen & Adeli  
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PATENT NO. 6,721,929 B2

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AUG 27 2004